

3-to-8 Decoder

1(a).

Input			Output							
Z	Y	X	Do	D1	D2	D3	D4	D5	D6	D7
0	0	0	1	0	0	0	0	0	0	0
0	0	1	0	1	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0
1	0	1	0	0	0	0	0	1	0	0
1	1	0	0	0	0	0	0	0	1	0
1	1	1	0	0	0	0	0	0	0	1

Priority Encoder

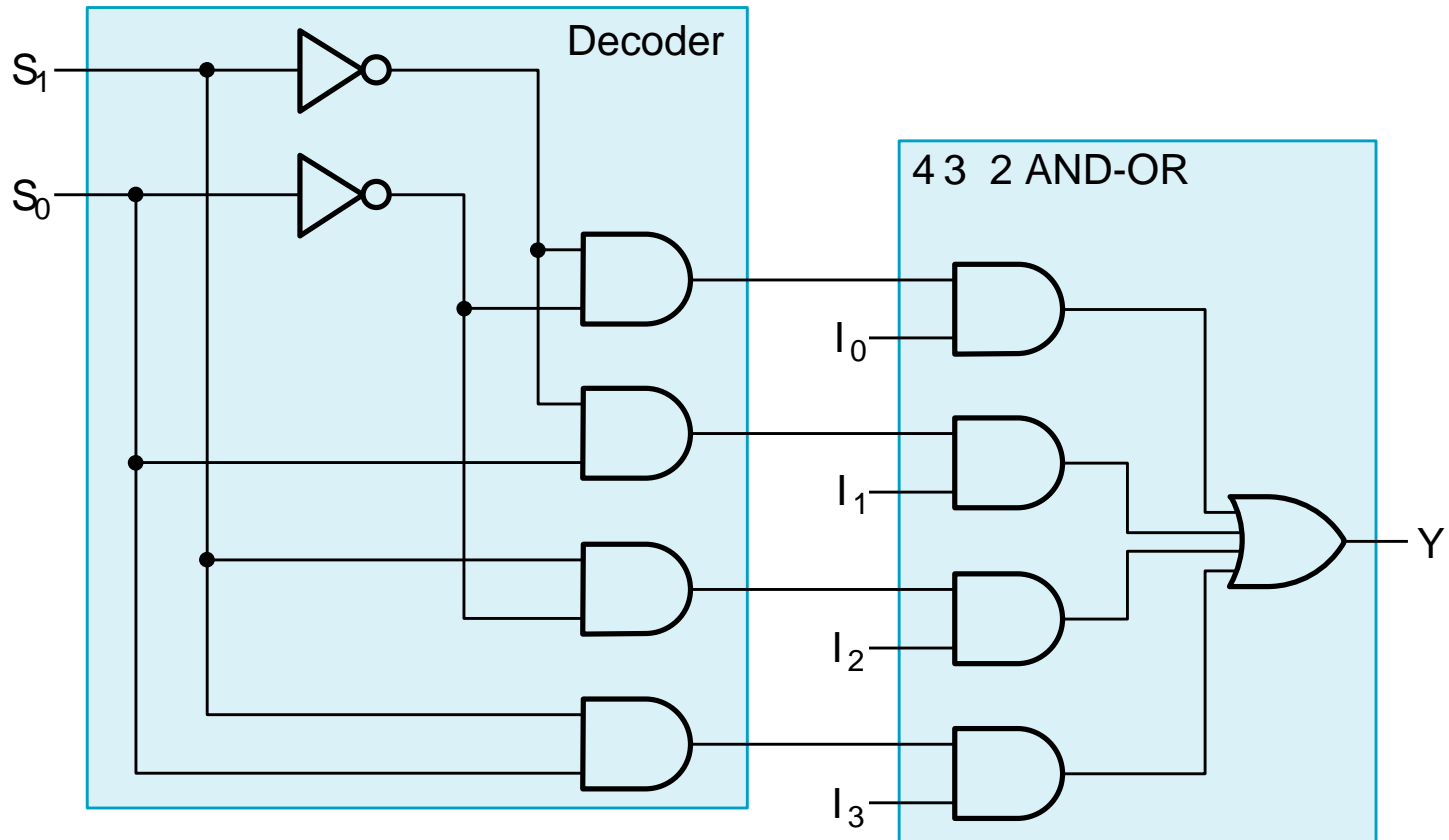
1(b).

Inputs				Outputs		
D ₀	D ₁	D ₂	D ₃	Y ₁	Y ₀	V
0	0	0	0	×	×	0
1	0	0	0	0	0	1
×	1	0	0	0	1	1
×	×	1	0	1	0	1
×	×	×	1	1	1	1

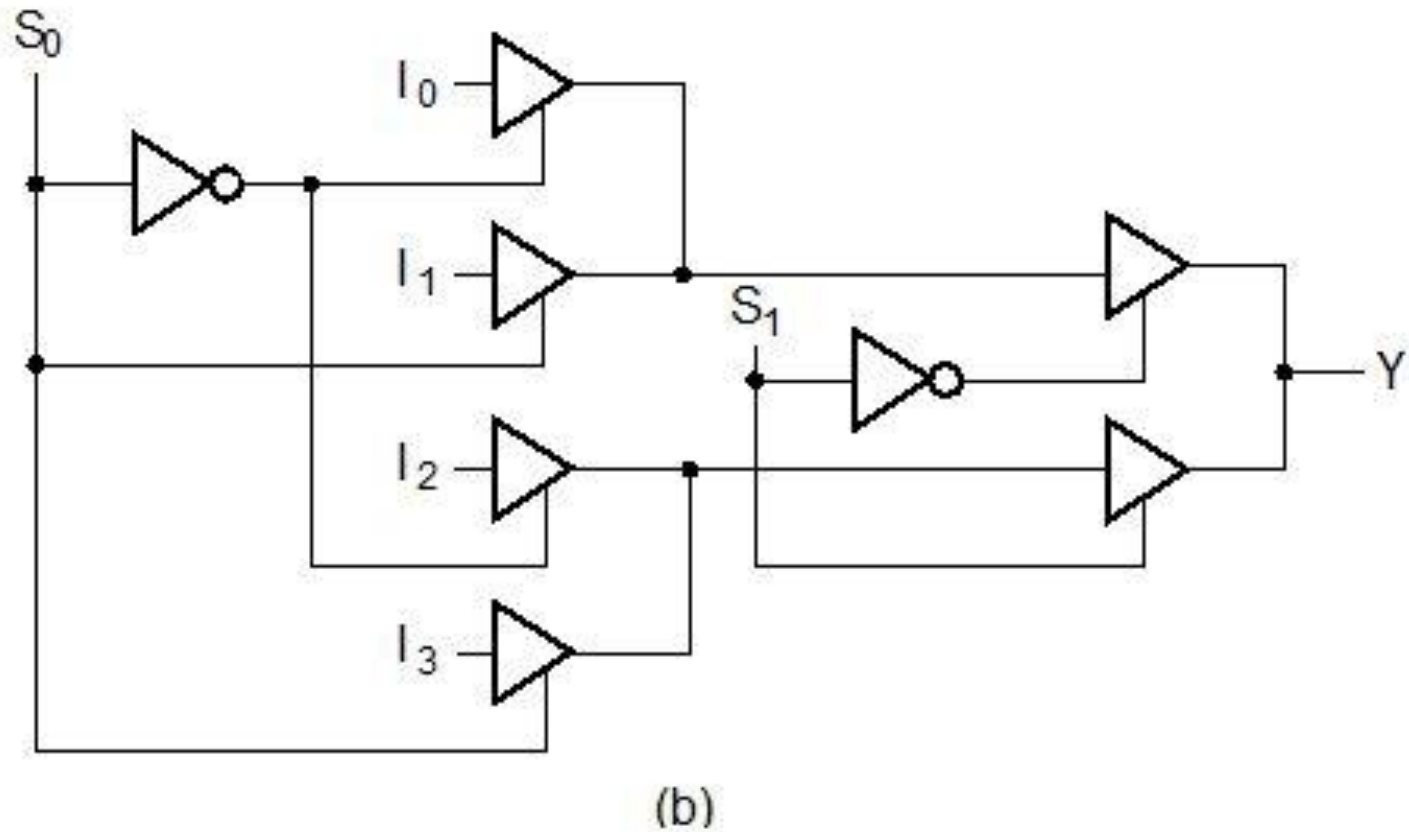
Priority encoder with 4 inputs (D₃, D₂, D₁, D₀) - highest priority to most significant 1 present - Code outputs Y₁, Y₀ and V where V indicates at least one 1 present.

4-to-1 Multiplexer

2&3.



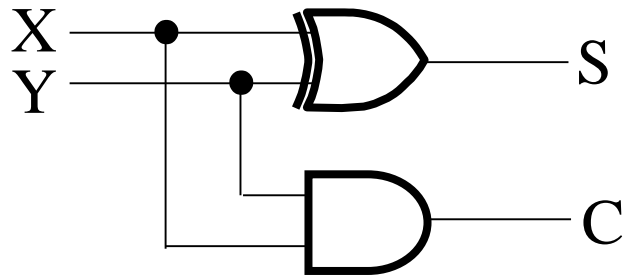
4-to-1 Multiplexer(con.)



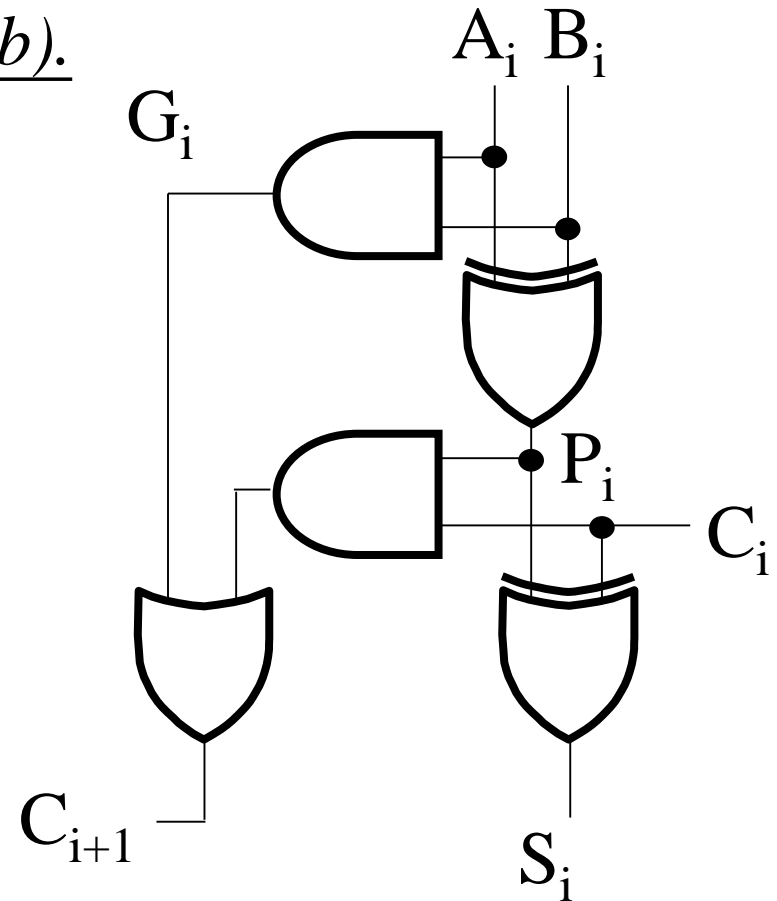
Half-Adder

Full Adder

4(a).



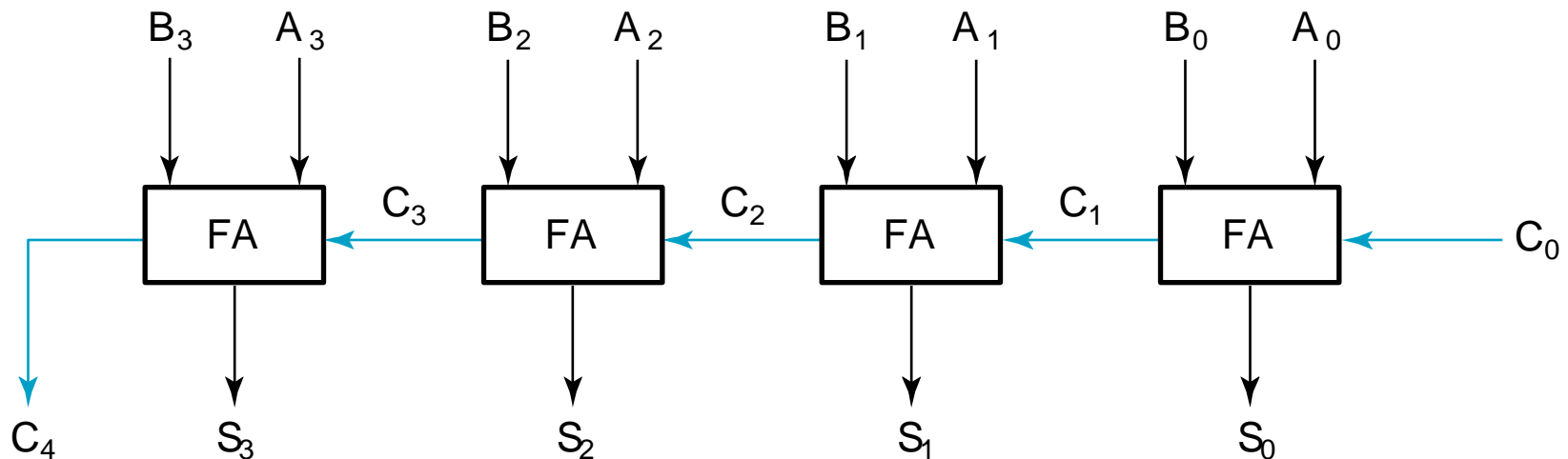
4(b).



4-bit Ripple-Carry Binary Adder

4(c).

- A four-bit Ripple Carry Adder made from four 1-bit Full Adders:



unsigned binary number subtraction

5.

$$(a) \underline{X - Y = 0010001},$$

$$(b) \underline{Y - X = -0010001}$$

Unsigned 2's Complement Subtraction Example 1

5(a).

- Find $1010100_2 - 1000011_2$

$$\begin{array}{r} 1010100 \\ - 1000011 \\ \hline \end{array} \xrightarrow{\text{2's comp}} \begin{array}{r} 11010100 \\ + 0111101 \\ \hline 0010001 \end{array}$$

- The carry of 1 indicates that no correction of the result is required.

Unsigned 2's Complement Subtraction Example 2

5(b).

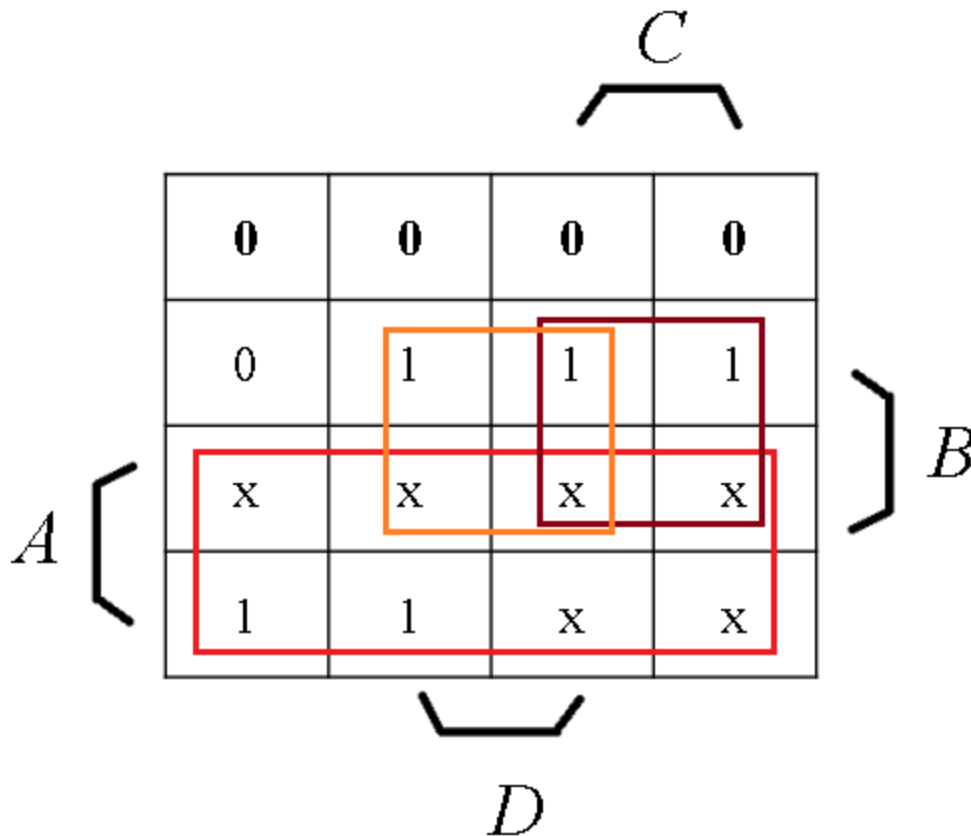
- Find $1000011_2 - 1010100_2$

$$\begin{array}{r} 1000011 \\ - 1010100 \\ \hline \end{array} \xrightarrow{\text{2's comp}} \begin{array}{r} 01000011 \\ + 0101100 \\ \hline 1101111 \\ \hline 0010001 \end{array} \xrightarrow{\text{2's comp}}$$

- The carry of 0 indicates that a correction of the result is required.
- Result = $-(0010001)$

unsigned binary number subtraction

6.



$$W(A,B,C,D) = A + BD + BC$$